

CONTENTS

Preface v

1. AN INTRODUCTION TO NUMBER SYSTEMS 1

- 1-1. *An Unstructured, Integer Number System* 2
- 1-2. *Features of the Decimal Number System* 3
- 1-3. *Discussion of Some Properties of Number Systems* 4
- 1-4. *General Characteristics of Weighted Systems for Computation* 7
- 1-5. *General Characteristics of the Residue Number System* 8
- 1-6. *Arithmetic Operations in a Number System* 9
- 1-7. *References* 11

2. THE ALGEBRA OF RESIDUE CLASSES 12

- 2-1. *Residue Representation* 12
- 2-2. *Identities Involving Residues and Integer Values* 16
- 2-3. *Multiplicative Inverse* 21
- 2-4. *Conversion Using the Chinese Remainder Theorem* 27
- 2-5. *Representation of Negative Numbers* 32
- 2-6. *References* 33

3. SOME SIMPLE RESIDUE ARITHMETIC OPERATIONS 34

- 3-1. *Addition and Subtraction in the Residue Representation* 34
- 3-2. *Multiplication in the Residue Representation* 36
- 3-3. *Definitions for Division in the Residue Representation* 37
- 3-4. *Division Remainder Zero* 38
- 3-5. *Conversion to the Residue Representation* 39
- 3-6. *Mixed-radix Conversion Process* 41
- 3-7. *Extension of Base* 47
- 3-8. *Other Frequently Used Operations* 50
- 3-9. *References* 51

4. SIGN DETECTION AND RELATIVE-MAGNITUDE DETERMINATION 52

- 4-1. *Sign Representation* 52
- 4-2. *Sign Detection by Converting to Weighted Number Systems* 54

- 4-3. *A Limitation on Sign-detection Methods: Two Coding Theorems* 56
- 4-4. *Applications of the Coding Theorems* 65
- 4-5. *Sequential Sign Determination* 67
- 4-6. *Other Sign-detection Schemes* 71
- 4-7. *Relative-magnitude Determination* 71
- 4-8. *References* 73

5. SCALING 74

- 5-1. *Scaling Positive Integers* 75
- 5-2. *Scaling Numbers of Either Sign* 79
- 5-3. *Rounding to the Closest Integer* 81
- 5-4. *References* 84

6. GENERAL DIVISION 85

- 6-1. *General Division I* 85
- 6-2. *General Division II* 91
- 6-3. *References* 95

7. OVERFLOW DETECTION 96

- 7-1. *Detecting Overflow Due to Addition or Subtraction in Nonredundant Systems* 96
- 7-2. *Detecting Additive or Subtractive Overflow Using a Redundant Modulus* 97
- 7-3. *Multiplicative-overflow in Nonredundant Systems* 98
- 7-4. *Detecting Multiplicative Overflow Using a Redundant Modulus* 100
- 7-5. *References* 104

8. RESIDUE DIGIT REPRESENTATION AND PROCESSING 105

- 8-1. *Reducing the Residue Digit Arithmetic Truth Table* 105
- 8-2. *Coding the Residue Digits* 109
- 8-3. *The Symmetric Residue System* 113
- 8-4. *Factoring the Moduli* 117
- 8-5. *Multiplication Using Index Calculus* 118
- 8-6. *References* 122

9. RESIDUE DIGIT LOGIC 123

- 9-1. *Matrix Arithmetic Units* 123
- 9-2. *Two Published Studies on Gating Complexity* 130
- 9-3. *Feedback Adders* 131
- 9-4. *References* 133

10. RESIDUE NUMBER REPRESENTATION AND PROCESSING 134

- 10-1. *Selection of Moduli* 134
- 10-2. *Number Representation and Operation Times* 140
- 10-3. *References* 150

11. RESIDUE COMPUTING SYSTEMS 151

- 11-1. *A Computer for Solving Linear Simultaneous Equations* 151
- 11-2. *A Digital Correlator* 153
- 11-3. *The RNS Computer* 155
- 11-4. *References* 156

12. SOME COMPUTATIONS WITH RESIDUE ARITHMETIC 158

- 12-1. *Discussion of Easy and Difficult Arithmetic Operations* 158
- 12-2. *Effect of Integer Representation* 160
- 12-3. *Evaluating Polynomials* 160
- 12-4. *Square-root Algorithm Using Linear Correction* 164
- 12-5. *A Method for Solving Linear Simultaneous Equations* 167
- 12-6. *References* 169

13. CONVERSION BETWEEN DIGITAL RESIDUE NUMBERS AND ANALOG QUANTITIES 170

- 13-1. *Residue Digital - to - Analog Conversion* 171
- 13-2. *Analog - to - Residue Digital Conversion* 174
- 13-3. *References* 176

14. ERROR-CHECKING CODES FOR WEIGHTED NUMBER SYSTEMS 177

- 14-1. *Terminology and Definitions* 177
- 14-2. *Systematic Codes* 180
- 14-3. *Nonsystematic Codes* 189
- 14-4. *References* 194

15. ERROR-CHECKING CODES FOR RESIDUE NUMBER SYSTEMS 195

- 15-1. *Multiplicity of Error* 195
- 15-2. *Single-error-detection Code* 195
- 15-3. *Higher-order Codes* 196
- 15-4. *Implementing a Residue Check Unit* 197
- 15-5. *References* 198

**16. THE APPLICATION OF RESIDUE TECHNIQUES TO WEIGHTED
NUMBER SYSTEMS 199**

- 16-1. *A Decimal Adder Using Mod-2 and Mod-5 Digits* 199
- 16-2. *The EPOS Computer* 202
- 16-3. *A Hybrid System Using Both Residue and Binary Numbers* 210
- 16-4. *Programming a Conventional Machine to Perform
Residue Computations* 212
- 16-5. *References* 216

ANNOTATED BIBLIOGRAPHY 217

- Name Index* 225
- Subject Index* 227

