

Contents

Preface

v

PART 1 MICROPROCESSOR SYSTEMS

| | | |
|----------|---|-----------|
| 1 | Introduction to Microprocessor Systems | 3 |
| 1.1 | Introduction to Microprocessor Applications | 3 |
| 1.2 | The Structure of Microprocessor Systems: Hardware | 8 |
| 1.2.1 | Memory | 9 |
| 1.2.2 | Processor | 12 |
| 1.2.3 | Input/Output devices | 15 |
| 1.2.4 | System configuration | 22 |
| 1.3 | The Structure of Microprocessor Systems: Software | 24 |
| 1.3.1 | Device handlers | 24 |
| 1.3.2 | Processor management | 25 |
| 1.3.3 | Memory management | 26 |
| 1.3.4 | Device and data management | 30 |
| 1.3.5 | Error handling routines and utilities | 32 |
| 1.3.6 | Application programs | 32 |
| 1.4 | Outline of Microprocessor Systems Development | 34 |
| | References | 39 |
| 2 | Digital Electronic Devices | 41 |
| 2.1 | Logic Gates | 41 |
| 2.2 | Boolean Algebra | 47 |
| 2.3 | Simple Logic Devices | 52 |
| 2.4 | Logic Circuits from NAND/NOR Gates | 58 |
| 2.5 | Simple Sequential Circuits | 63 |
| 2.6 | Logic Families | 68 |
| | References | 71 |
| 3 | The Logical Design of Boolean Devices | 72 |
| 3.1 | Synthesis and Simplification Procedures | 72 |
| 3.2 | Examples of Boolean Arithmetic Modules | 76 |

| | | |
|----------|--|------------|
| 3.2.1 | Decimal adder | 76 |
| 3.2.2 | Carry look-ahead adder | 78 |
| 3.2.3 | Comparators | 81 |
| 3.3 | Examples of Boolean Sequential Modules | 84 |
| 3.3.1 | Registers | 84 |
| 3.3.2 | Counters | 87 |
| 3.3.3 | Sequential multiplier | 89 |
| 3.3.4 | Floating point adder | 91 |
| 3.4 | Large-Scale Integration Devices | 93 |
| 3.4.1 | Large memory modules | 93 |
| 3.4.2 | Shift register stores | 96 |
| 3.4.3 | Parallel vector processing devices | 97 |
| 3.4.4 | Microprocessors | 100 |
| | References | 102 |
| 4 | Microprocessor Programming | 103 |
| 4.1 | Overview of Microprocessors | 103 |
| 4.2 | Introducing the Intel 8080 Microprocessor | 109 |
| 4.2.1 | Program counter | 111 |
| 4.2.2 | Branches and condition codes | 118 |
| 4.2.3 | Stack pointer | 121 |
| 4.3 | Intel 8080 Machine Instructions | 125 |
| 4.3.1 | Data movement instructions | 127 |
| 4.3.2 | Jump instructions | 129 |
| 4.3.3 | Accumulator manipulation instructions | 130 |
| 4.3.4 | Register manipulations | 135 |
| 4.3.5 | Miscellaneous | 136 |
| 4.4 | Assembly Programming | 136 |
| 4.4.1 | Pseudo-codes and two-pass assemblers | 137 |
| 4.4.2 | Macros | 140 |
| 4.4.3 | Use of subroutines | 142 |
| 4.4.4 | Final example | 144 |
| 4.5 | The Software Development Process | 147 |
| 4.6 | Directions for Microprocessor Architecture Development | 149 |
| | References | 152 |
| 5 | Microprocessor Input/Output Handling | 153 |
| 5.1 | Simple I/O Systems | 153 |
| 5.1.1 | Basic I/O hardware | 155 |
| 5.1.2 | Simple I/O programming | 161 |
| 5.1.3 | Intel 8085 and other manufacturers' I/O systems | 164 |
| 5.2 | Interrupt Systems | 166 |
| 5.2.1 | Intel 8080 interrupt handling | 169 |
| 5.2.2 | Interrupt control hardware | 172 |
| 5.2.3 | Interrupt programming | 178 |
| 5.3 | Direct Memory Access Systems | 183 |
| 5.4 | Interfacing Standards | 186 |
| 5.4.1 | RS232 interface | 187 |
| 5.4.2 | IEEE-488 bus | 187 |

| | |
|---|------------|
| 5.4.3 Intel multibus | 190 |
| References | 193 |
| 6 Development Systems | 194 |
| 6.1 Software Development Aids | 195 |
| 6.1.1 The process of software development | 195 |
| 6.1.2 Assemblers | 196 |
| 6.1.3 High-level languages | 198 |
| 6.1.4 Simulators | 201 |
| 6.1.5 Microprocessor operating systems | 202 |
| 6.2 Hardware Development Aids | 204 |
| 6.2.1 The process of hardware development | 204 |
| 6.2.2 Hardware design aids | 205 |
| 6.2.3 Hardware testing and debugging aids | 207 |
| References | 211 |

PART 2 SIGNAL PROCESSING APPLICATIONS

| | |
|---|------------|
| 7 Microprocessors in Signal Processing | 215 |
| 7.1 Introduction | 215 |
| 7.2 Microprocessor Signal Handling | 216 |
| 7.3 Data Logging | 218 |
| 7.4 Transformation | 223 |
| 7.4.1 The fast Fourier transform algorithm | 224 |
| 7.4.2 FFT processors | 230 |
| 7.5 Correlation | 234 |
| 7.5.1 Correlation using the FFT | 235 |
| 7.5.2 Bit-by-bit correlation | 237 |
| 7.5.3 Polarity coincidence correlation | 239 |
| 7.6 The Programmable Signal Processor | 241 |
| 7.6.1 Hardware design | 242 |
| 7.6.2 Software for the PSP | 246 |
| References | 247 |
| 8 Analog/Digital Operations | 249 |
| 8.1 Introduction | 249 |
| 8.2 Analog/Digital Conversion | 249 |
| 8.2.1 The operational amplifier | 250 |
| 8.2.2 The digital/analog converter | 253 |
| 8.2.3 The analog/digital converter | 255 |
| 8.2.4 Controlling the conversion process | 259 |
| 8.3 Analog I/O Connection | 262 |
| 8.4 Digital Filtering | 266 |
| 8.4.1 Design considerations | 268 |
| 8.4.2 The IIR filter | 269 |
| 8.4.3 The FIR filter | 278 |
| References | 281 |

| | |
|--|------------|
| 9 Applications | 282 |
| 9.1 Acoustic Imaging | 282 |
| 9.1.1 Historical perspective | 282 |
| 9.1.2 Phased arrays | 286 |
| 9.1.3 Acoustic holography | 287 |
| 9.1.4 An acoustic imaging system | 290 |
| 9.1.5 The phased array transmitter | 292 |
| 9.1.6 The holographic receiver | 293 |
| 9.1.7 System control | 297 |
| 9.2 Data Logging of Solar Activity | 297 |
| 9.2.1 Measurement of solar activity | 299 |
| 9.2.2 A microprocessor-based riometer unit | 300 |
| 9.2.3 The replay unit | 306 |
| 9.2.4 Summary | 306 |
| References | 307 |
| Index | 308 |