

# CONTENTS

## PREFACE vii

### chapter 1 INTRODUCTION 1

- 1-1 Plan of Attack 1
- 1-2 Pulse Signals; Information Coding 4
- 1-3 Some Generalities 8
- 1-4 Elementary Pulse Signals 10
- 1-5 Pulse Distortion and Bandwidth 13
- 1-6 Thévenin's and Norton's Theorems 17
- 1-7 Addition of Voltages; Addition of Currents 22
- 1-8 Duality 24

### appendix 1 26

- A1-1 Binary Notation 26
- A1-2 Units of Measurement; Notation 27
- A1-3 Circuit Diagrams 28

### Problems 30

### chapter 2 LINEAR CIRCUIT ELEMENTS AND SIMPLE NETWORKS 33

- 2-1 High-pass and Low-pass  $R$ - $C$  Networks 34
- 2-2 Rules for the Transient Behavior 36
- 2-3 Rules for the DC Component 38

2-4	Filling in the Exponential Waveform	39
2-5	Single-time-constant Networks	43
2-6	More Complicated Waveshapes	48
2-7	Pseudo-differentiation and Pseudo-integration	53
2-8	<i>L-C-R</i> Circuits	56
2-9	Transmission Lines	60
2-10	Delay-line Pulse Shaping	67
2-11	Pulse Transformers	72

## **appendix 2 78**

A2-1	Transient Response of Resonant Circuits	78
A2-2	Properties of Uniform Rectilinear Lines	80
A2-3	Delay Cable	82
A2-4	Lumped-constant Delay Lines	83

## **Problems 85**

## **chapter 3 DIODES 89**

3-1	The Junction Diode	90
3-2	The Point-contact Diode	93
3-3	Diode Ratings	94
3-4	Transient Response of Diodes	94
3-5	The Thermionic Diode	98
3-6	Diode Circuit Notes	99
3-7	Combination of Diodes and Resistors: Aperiodic Clipping and Pickoff Circuits	100
3-8	Clipping Circuits with Capacitors: Nonlinear Pulse Shaping	104
3-9	Pickoff Circuits with Capacitors: Pulse Stretching	109
3-10	DC Restoration	113
3-11	DC-restoration Problems	116
3-12	Diode Circuits with Inductors	117
3-13	Special Diode Applications	119

## **Problems 121**

## **chapter 4 CHARACTERISTICS OF VACUUM TRIODES 124**

4-1	Two-ports	125
4-2	Ideal Active Elements	126
4-3	Triode Characteristics	127
4-4	Loadlines	128
4-5	Incremental Tube Parameters	130
4-6	Dependence and Range of Triode Parameters	133

- 4-7 Choice of Quiescent Point 135
- 4-8 The Positive-grid Region 135
- 4-9 Triodes in Switching Service 137
- 4-10 Other Triode Topics 138

#### **appendix 4 139**

- A4-1 Characteristics of Type 6DJ8/ECC88 Double Triode 139
  - A4-2 Characteristics of Type 12AX7A Double Triode 142
- Problems 144**

### **chapter 5 TRIODE JUNCTION TRANSISTORS 146**

- 5-1 Transistor Fundamentals 147
- 5-2 Saturation 152
- 5-3 The Emitter Junction 154
- 5-4 Voltage Control 155
- 5-5 The Bilateral Two-port 156
- 5-6 Dependence of Transistor Parameters 159
- 5-7 Large-signal Conditions 160
- 5-8 Transistor Ratings 161
- 5-9 Dependence of Gain on Frequency 162
- 5-10 Transient Response 163
- 5-11 Charge Control 165
- 5-12 Junction Capacitances 167
- 5-13 Response Times with Current Control 168
- 5-14 Transient Charge Drive 170

#### **appendix 5 172**

- A5-1 Specifications of Some Selected Transistors 172
- A5-2 Construction of Transistor Circuits 184
- A5-3 Transistor Breakdown Characteristics 186
- A5-4 The Tube-Transistor Relationship; Interaction of Input and Output Impedances 188

**Problems 191**

### **chapter 6 BIASING 193**

- 6-1 Tube Drifts 195
- 6-2 Component Drifts 196
- 6-3 Choice of Quiescent Conditions 197
- 6-4 Cathode Bias 201
- 6-5 Bypassing 204
- 6-6 Anode Bottoming 207

- 6-7 Voltage-feedback Bias 209
- 6-8 Transistor Drifts 212
- 6-9 Emitter Bias 213
- 6-10 Saturation 217
- 6-11 Voltage-feedback Bias 217
- 6-12 Self-bias and Fixed Bias 219
- 6-13 Cutoff 223
- 6-14 More Complex Feedback Bias Circuit 227
- 6-15 Summary 227

**appendix 6 229**

- A6-1 Tube and Transistor Impedance Relationships 229
- Problems 231

**chapter 7 THE PENTODE 234**

- 7-1 The Miller Effect 235
- 7-2 The Pentode 236
- 7-3 Pentode Disadvantages 240
- 7-4 The Gain-Bandwidth Product (Gain/Risetime Ratio) 241
- 7-5 Pentode Biasing 243
- 7-6 Dual-control Pentodes 247

**appendix 7 248**

- A7-1 Characteristics of Type 6688/E180F Pentode 248
- Problems 251

**chapter 8 OTHER TRIODE CONFIGURATIONS 252**

- 8-1 The Grounded-base Configuration 253
- 8-2 Biasing the Grounded-base Amplifier 255
- 8-3 The Grounded-collector Configuration 257
- 8-4 The Cathode Follower 258
- 8-5 The Emitter Follower 260
- 8-6 Biasing Emitter or Cathode Followers 262
- 8-7 Large-signal Operation of Emitter Followers 263
- 8-8 Cascaded Emitter Followers; the Darlington Configuration 267
- 8-9 Darlington Constant-current Source 269
- 8-10 The Cascode 270
- 8-11 Cascode Biasing 271
- 8-12 Emitter-coupled Pairs 272
- 8-13 Biasing the Emitter-coupled Pair 275

- 8-14 Difference Amplifier and Phase Splitter 276  
8-15 Drift in the Difference Amplifier 279

## **appendix 8 280**

- A8-1 The Emitter-coupled Pair 280

**Problems 283**

## **chapter 9 NONLINEAR MISCELLANY. I. SWITCHING, LOGIC GATES 287**

- 9-1 The Overdriven Amplifier 288  
9-2 Current-mode Circuits 294  
9-3 Logic Gates: Diodes 296  
9-4 Emitter-coupled Gates 300  
9-5 Saturating Transistor Gates 302  
9-6 Pulse Shaping 304

**Problems 308**

## **chapter 10 NEGATIVE FEEDBACK 310**

- 10-1 The Feedback Loop 311  
10-2 Gain Stabilization 314  
10-3 Voltage and Current Feedback; Output Impedance 315  
10-4 The Comparator; Input Impedance 316  
10-5 Feedback Configurations 318  
10-6 Effect on Noise and Drift 320  
10-7 Stability 322  
10-8 The Effect of Overload on Feedback Systems 330  
10-9 Positive Feedback within Negative-feedback Loops 330

## **appendix 10 333**

- A10-1 Minimum-phase-shift Networks 333  
A10-2 Control of stability 335

**Problems 340**

## **chapter 11 NEGATIVE-FEEDBACK AMPLIFIERS 342**

- 11-1 The Comparator for Voltage-controlled Input 343  
11-2 Emitter-coupled Input Stage 344  
11-3 Emitter Follower, Stacked-up Emitter Follower; Feedback to Ground-return Lead of Amplifier Input 346  
11-4 Impedance-buffered Addition; the Collector Follower 351  
11-5 Biasing the Collector Follower 355

- 11-6 Operational Amplifiers 357
- 11-7 Negative Current Feedback 361
- 11-8 Nonlinear Feedback Networks 364

Problems 367

## **chapter 12 POSITIVE FEEDBACK 370**

- 12-1 Standardization of Pulse Amplitude; Discriminators 371
- 12-2 Backlash and Memory; Time Dependence 374
- 12-3 Monostable Circuits 376
- 12-4 Astable Trigger Circuits 377
- 12-5 Production of Positive Feedback 378

## **chapter 13 BISTABLE TRIGGER CIRCUITS 381**

- 13-1 The Schmitt Circuit 381
- 13-2 Threshold Drift; Backlash Reduction 386
- 13-3 Transistor Schmitt 389
- 13-4 Choice of Current Level 391
- 13-5 Cross-coupled Binaries 392
- 13-6 DC Couplings for Transistors 393
- 13-7 DC Couplings for Tubes 395
- 13-8 Triggering 397
- 13-9 Scaling 401
- 13-10 Nonsaturating Transistor Binaries 403
- 13-11 Loading of Binaries 404
- 13-12 Complementary Pairs 406
- 13-13 The Transitron 407

Problems 408

## **chapter 14 MONOSTABLE AND ASTABLE TRIGGER CIRCUITS 410**

- 14-1 Timing by *R-C* Coupling 411
- 14-2 Emitter-coupled Univibrators 413
- 14-3 Cross-coupled Univibrators 418
- 14-4 Two AC Couplings (Monostable) 423
- 14-5 Frequency Division 424
- 14-6 Multivibrators 426
- 14-7 Synchronization 430
- 14-8 Gated Multivibrator 431

Problems 432

**chapter 15 THE BLOCKING OSCILLATOR 435**

- 15-1 The Three Blocking Oscillator Configurations 436
- 15-2 The Monostable Blocking Oscillator 437
- 15-3 Current and Voltage Limitation 439
- 15-4 Triggering; Extraction of Output 441
- 15-5 Risetime; Blocking; Control of Pulse Length 444
- 15-6 The Astable Blocking Oscillator 447
- 15-7 Tube Blocking Oscillators 448
- 15-8 The Multiar 450

**Problems 452**

**chapter 16 RAMP GENERATORS; TIME CODING 453**

- 16-1 Basic Techniques 455
- 16-2 Control of Ramp Cycle 458
- 16-3 Current-source Sweeps 460
- 16-4 Bootstrap Sweeps 466
- 16-5 Miller Sweeps 468
- 16-6 Dual-control Pentode Sweeps; the Phantastron 472
- 16-7 Retrace Problems 474
- 16-8 Amplitude-to-Time Conversion; Time Coding 477
- 16-9 Time-to-Amplitude Conversion 481

**Problems 483**

**chapter 17 COINCIDENCE AND TRANSMISSION GATES 486**

- 17-1 Diode Gates 489
- 17-2 Emitter-coupled Gates 493
- 17-3 Saturating Transistor Gates; Diode-Bridge Gates 495
- 17-4 Coincidence Gating and Time Definition 498
- 17-5 Tunnel-diode Pulse Shaping 500

**Problems 502**

**chapter 18 NONLINEAR MISCELLANY. II. REGISTERS, COUNTERS, AND MATRICES 504**

- 18-1 Registers; Readin and Readout; Transfer; Shifting 505
- 18-2 Counting 508
- 18-3 Ring Counters; Special Counting Devices 511
- 18-4 Decoding Matrices 512
- 18-5 Concluding Example 515

**Problems 516**

**Bibliography 519**

**Index 523**