

Contents

Chapter 1.

FACTORS TO CONSIDER IN SELECTING A LINEAR INTEGRATED
CIRCUIT 1

by Jerry Eimbinder

1.1 Rule 1: Do Not Overspecify. 2

1.2 Rule 2: Check the Safety Features 2

1.3 Rule 3: Weigh Cost Against Performance 3

1.4 Rule 4: Consider Frequency Compensation 3

1.5 Rule 5: Compare the Specifications of Various Vendors 4

1.6 Rule 6: Check the Product's History 5

1.7 Rule 7: Determine Whether the IC Lends Itself to Mass
Production 6

1.8 Rule 8: Consider the Probability of Wide Acceptance of
the IC 6

1.9 Rule 9: Check for Applicable Literature 6

1.10 Rule 10: Evaluate the Testing Aspects 7

References 7

Chapter 2.

INTERPRETING OPERATIONAL-AMPLIFIER SPECIFICATIONS 8

by Ray Stata

2.1 The Lack of Standards 8

2.2 Voltage Drift 9

2.3 Thermal Considerations 9

2.4 Zeroing Can Hide Second-Order Defects 11

2.5 Average Drift 12

2.6 Output 13

2.7	Common-Mode Specifications	14
2.8	Full-Power Response	15
2.9	Settling Time	17

Chapter 3.

APPLICATIONS FOR OPERATIONAL AMPLIFIERS	19
--	-----------

by William Routh

3.1	The Inverting Amplifier	20
3.2	The Noninverting Amplifier	21
3.3	The Unity-Gain Buffer	22
3.4	The Summing Amplifier	23
3.5	The Difference Amplifier	23
3.6	Differentiator	24
3.7	Integrator	25
3.8	Simple Low-Pass Filter	26
3.9	The Current-to-Voltage Converter	27
3.10	Photocell Amplifiers	28
3.11	Precision Current Source	29
3.12	Adjustable Voltage References	30
3.13	The Reset Stabilized Amplifier	32
3.14	Analog Multiplier	33
3.15	Full-Wave Rectifier and Averaging Filter	34
3.16	Sine-Wave Oscillator	36
3.17	Triangle-Wave Generator	37
3.18	Tracking Regulated Power Supply	38
3.19	Programmable Bench Power Supply	40
3.20	Conclusions	41
	References	41

Chapter 4.

APPLICATIONS FOR FULLY COMPENSATED OPERATIONAL AMPLIFIERS.	42
---	-----------

by Michael English

4.1	Voltage Regulator.	42
4.2	Integrator	43
4.3	Differentiator	45

4.4	Voltage-Regulator Reference Amplifier	46
4.5	High-Voltage Regulated Power Supply	47
4.6	Clipping Amplifier	48
4.7	Comparator	49

Chapter 5.

UNDERSTANDING FULLY COMPENSATED IC OPERATIONAL AMPLIFIERS.	51
---	-----------

by Harry Gill

5.1	The Input Stage	52
5.2	The Second Stage	54
5.3	The Output Stage	55
5.4	Biasing Circuits	57
5.5	Frequency Compensation	61
5.6	Derivation of Gain	62
	References	64

Chapter 6.

HIGH-FREQUENCY CHARACTERISTICS OF WIDEBAND INVERTER OPERATIONAL AMPLIFIERS.	65
--	-----------

by Heinrich Krabbe

6.1	Calculating Frequency Response	65
6.2	Improving Frequency Response	69
6.3	Settling Time	71
6.4	Slew Rate	72
6.5	Loop Gain versus Closed-Loop Accuracy at High Frequencies.	73

Chapter 7.

POPULAR LINEAR-INTEGRATED-CIRCUIT APPLICATIONS	75
---	-----------

by Leonard Brown and Ralph Seymour

7.1	Long-Time-Constant Monostable (One Shot)	75
7.2	Ramp Generator	77
7.3	Audio Preamplifier with RIAA/NAB Compensation	77

7.4	Tone-Control Circuit	78
7.5	Crystal Oscillator	78
7.6	Doubly Balanced Modulator	81

Chapter 8.

THE VERSATILE WIDEBAND IC AMPLIFIER	82
--	-----------

by Brent Welling

8.1	Circuit Description and Operation	82
8.2	Video Switch	86
8.3	Frequency Shift Keyer	88
8.4	Wideband Differential Amplifier with Automatic Gain Control	90
8.5	Amplitude Modulator	90
8.6	Balanced Modulator	93
8.7	Pulse Amplifier	95

Chapter 9.

APPLYING INTEGRATED CIRCUITS TO FM-DETECTOR

APPLICATIONS.	97
------------------------------	-----------

by W. F. Allen, Jr., and L. H. Hoke, Jr.

9.1	Integrated-Circuit Considerations	98
9.2	Foster-Seeley Discriminator	99
9.3	Ratio Detector	100
9.4	Counter Detector	102
9.5	RC Phase-Shift Discriminator	107
9.6	Quadrature Detector.	110
9.7	Crystal Discriminator	114
9.8	Applications	114
9.9	Conclusions.	116
	References	116

Chapter 10.

FM-DETECTOR-LIMITER INTEGRATED CIRCUITS 119

by Peter H. Mack and Norman S. Palazzini

10.1	The Detector-Limiter IC	119
10.2	Application in FM and Television Circuitry	121
10.3	Two-Block FM Intermediate-Frequency Strip	126
10.4	Suppressed Carrier Modulation.	128
10.5	Pulse Modulation	130
10.6	Balanced-Product Mixer	131
10.7	Synchronous AM Detection with Carrier Recovery	132
10.8	Phase Comparator	135
10.9	Wideband Amplifier	137

Chapter 11.

THE DUAL OPERATIONAL AMPLIFIER. 140

by David Long and David Campbell

11.1	DC Circuit Performance	140
11.2	Low-Frequency Gain	142
11.3	Bias Stability and Signal-Handling Capability	146
11.4	Power Dissipation	147
11.5	Short-Circuit Protection	147
11.6	Channel Isolation	149
11.7	Noise	150
11.8	Compensation	153
11.9	Operating Safeguards	161

Chapter 12.

THE LINEAR FOUR-QUADRANT MULTIPLIER 162

by Edward L. Renschler

12.1	Methods of Analog Multiplication	163
12.2	Variable Transconductance	164
12.3	Analysis of the IC	170
12.4	Using the Multiplier	172
12.5	Simple Applications	178
	References	180

Chapter 13.

MULTIPLEXING WITH COMPLEMENTARY MOS INTEGRATED CIRCUITS 181

by R. L. Sanquini and A. A. Ahmed

13.1	COS/MOS Construction	181
13.2	Basic Multiplexer Channel	182
13.3	<i>p</i> -Channel MOS Switch	183
13.4	Complementary Switch	185
13.5	Performance Characteristics.	188
13.6	Summary	190

Chapter 14.

VOLTAGE-REGULATOR ICs WITH FOLDBACK CURRENT LIMITING . 191

by Douglas R. Sullivan and Hans W. Mamie

14.1	Design Approaches	191
14.2	Current-Limiting Voltage Regulator	194
14.3	Basic Voltage Regulator	198
14.4	Frequency Compensation	198
14.5	Current Limiting.	199
14.6	Current-Expanded Positive Regulator	200
14.7	Current-Expanded Negative Regulator	200
14.8	Current-Expanded Negative Floating Regulator	202
14.9	Positive Floating Regulator	202
14.10	Current-Expanded Positive Floating Regulator	203
14.11	Remote Shutdown Regulator	204
14.12	External Reference Regulator	205
14.13	Switching Regulator	206
14.14	Conclusions	208

Chapter 15.

INTEGRATED VOLTAGE REGULATORS 209

by Edward L. Renschler

15.1	Design Features	210
15.2	Detailed Circuit Description.	212

15.3	Specifying a Monolithic Voltage Regulator	215
15.4	Regulation and Output Impedance	215
15.5	Thermal Considerations	217
15.6	How To Use the IC Regulator in Practical Circuits	219
15.7	Shutdown Techniques	221
15.8	Thermal Shutdown	222
15.9	Nonthermal Techniques	224
15.10	Voltage Boost	227
15.11	Current Boost	228
15.12	Switching Regulator	231
15.13	Zero-Temperature-Coefficients Reference	231
15.14	Power-Supply Applications	231
	References	234

Chapter 16.

POSITIVE VOLTAGE REGULATORS	235
--	------------

by Robert J. Widlar

16.1	An Improved Regulator	235
16.2	Circuit Description	237
16.3	Power Limitations	240
16.4	Using Booster Transistors	242
16.5	Foldback Current Limiting	244
16.6	High-Current Regulator	246
16.7	Dominant Failure Mechanisms	247
16.8	Conclusions	249
	References	250

Chapter 17.

NEGATIVE VOLTAGE REGULATORS	251
--	------------

by Robert J. Widlar

17.1	The Negative Regulator	251
17.2	Circuit Operation	251
17.3	Low-Power Regulator	254
17.4	Increased Output Current	255
17.5	Foldback Current Limiting	258
17.6	Symmetrical Power Supplies	259

17.7	Adjustable Current Limiting	259
17.8	Improving Line Regulation	261
17.9	Using Protective Diodes	261
17.10	High-Voltage Regulator	263
17.11	Switching Regulator	265
17.12	High-Current Switching Regulator	267
17.13	Switching Regulator with Current Limiting	269
17.14	Switching Regulator with Overload Shutoff	270
17.15	Driven Switching Regulator	271

Chapter 18.

A HIGH-SPEED DATA TRANSMISSION SYSTEM	273
--	------------

by Jerry Avery

18.1	Line-Driver Characteristics	273
18.2	Driver-Circuit Description	275
18.3	Line-Receiver-Circuit Description	281
18.4	Receiver-Input Considerations	285
18.5	Dual Differential Comparator	286
18.6	Furnace Control	287

Chapter 19.

DESIGNING WITH INTEGRATED COMPARATORS	288
--	------------

by Alfredo Gomez

19.1	Input Threshold	289
19.2	Threshold Variations with Power Supplies	290
19.3	Threshold Variations with Temperature	291
19.4	Signal Propagation and Output Rise Time	291
19.5	Common-Mode Recovery	294

Chapter 20.**PRACTICAL CONSIDERATIONS IN THE DESIGN OF SYSTEMS USING
LINEAR INTEGRATED CIRCUITS 296***by A. C. Markkula, Jr.*

20.1	Voltage Regulators	299
20.2	Operational Amplifiers	302
20.3	The Ideal Operational Amplifier	305
20.4	Inverting Amplifier	306
20.5	Noninverting Amplifier	307
20.6	Differential Amplifier	307
20.7	Feedback Elements	308
20.8	Sources of Error	309
20.9	Open-Loop Gain	316
20.10	Frequency Compensation for the $\mu A709$	319
20.11	Compensation	320
20.12	Recommended Networks	323
20.13	Rate Limiting	325
20.14	Supply Voltage and Temperature Effects	326
20.15	Basic Comparator Application	327
20.16	Double-Ended Limit Detector	329

INDEX	331
------------------------	------------